



THE UNITED STATES PATENT AND TRADEMARK OFFICE

#12 (B)
(NE)
3-10-04
[Signature]

Applicant(s): **Bortfeld, et al.**

Group Art Unit: 2123

Application Serial No.: **09/586,325**

Examiner: CRAIG, D. M.

Filed: **June 2, 2000**

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FEB 20 2004

Technology Center 2100

Title: **Method And Apparatus For
Accelerating Hardware
Simulation**

AMENDMENT AND RESPONSE TO *FINAL* REJECTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

This Amendment and Response is submitted in response to the *Final* Office Action dated November 28, 2003, in the above-referenced patent application. Please enter and consider the following amendments and remarks.